

FIG. 1 is a block diagram of a computer system architecture. The system includes a Processor (12) connected to a Memory Hub (18) via a bus (14). The Memory Hub (18) is connected to System Memory (22) via a bus (20). The Memory Hub (18) is also connected to an I/O Hub (34) via a bus (16). The I/O Hub (34) is connected to a Display Driver (30) and a Display (32) via a bus (26). The I/O Hub (34) is also connected to a NIC (60) and a network (TO NETWORK). The I/O Hub (34) is connected to a printer (52) and a storage device (54) via a bus (40). The I/O Hub (34) is connected to an I/O Controller (44) via a bus (42). The I/O Controller (44) is connected to a keyboard (48) and a mouse (46).

48

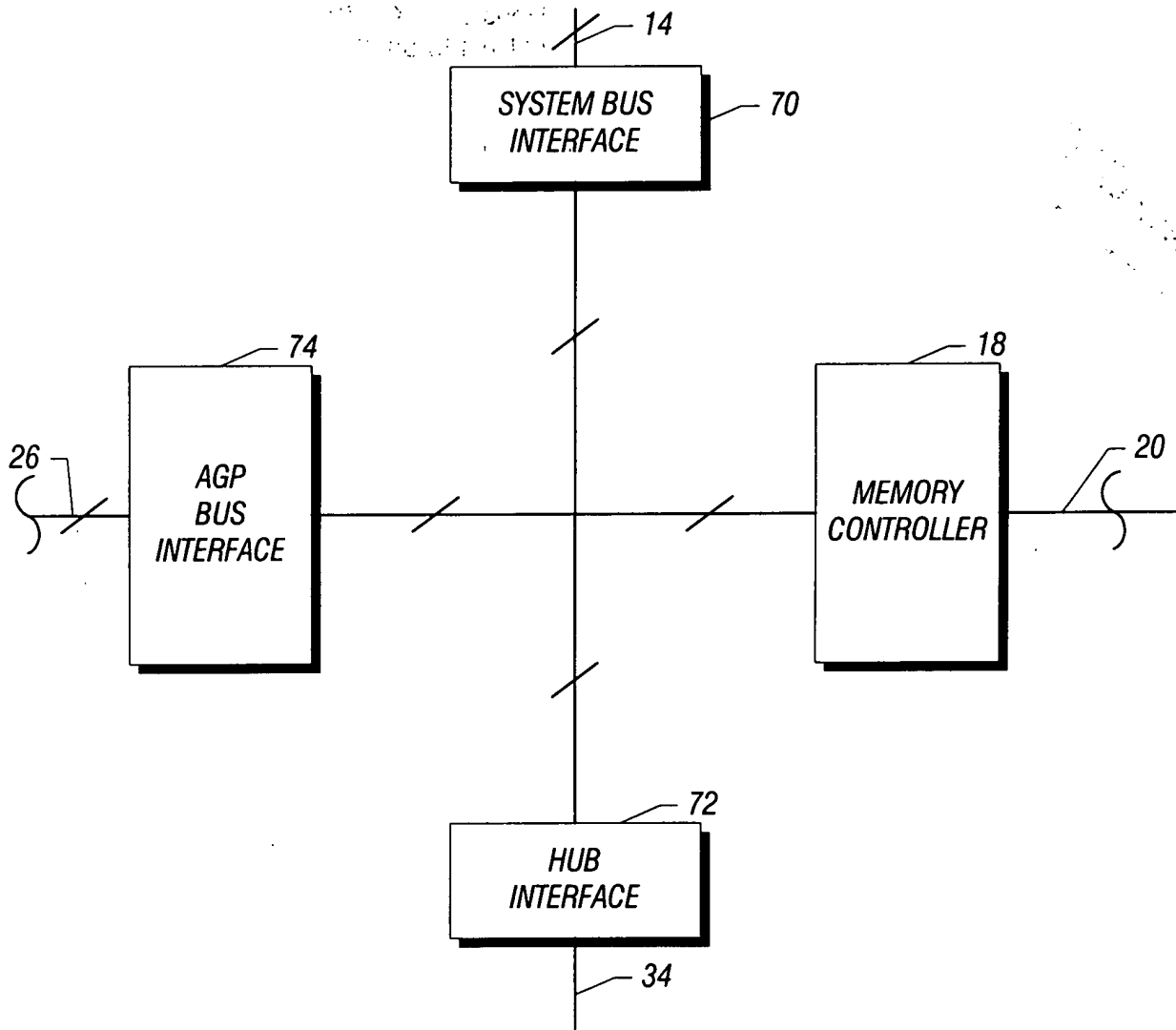


FIG. 2

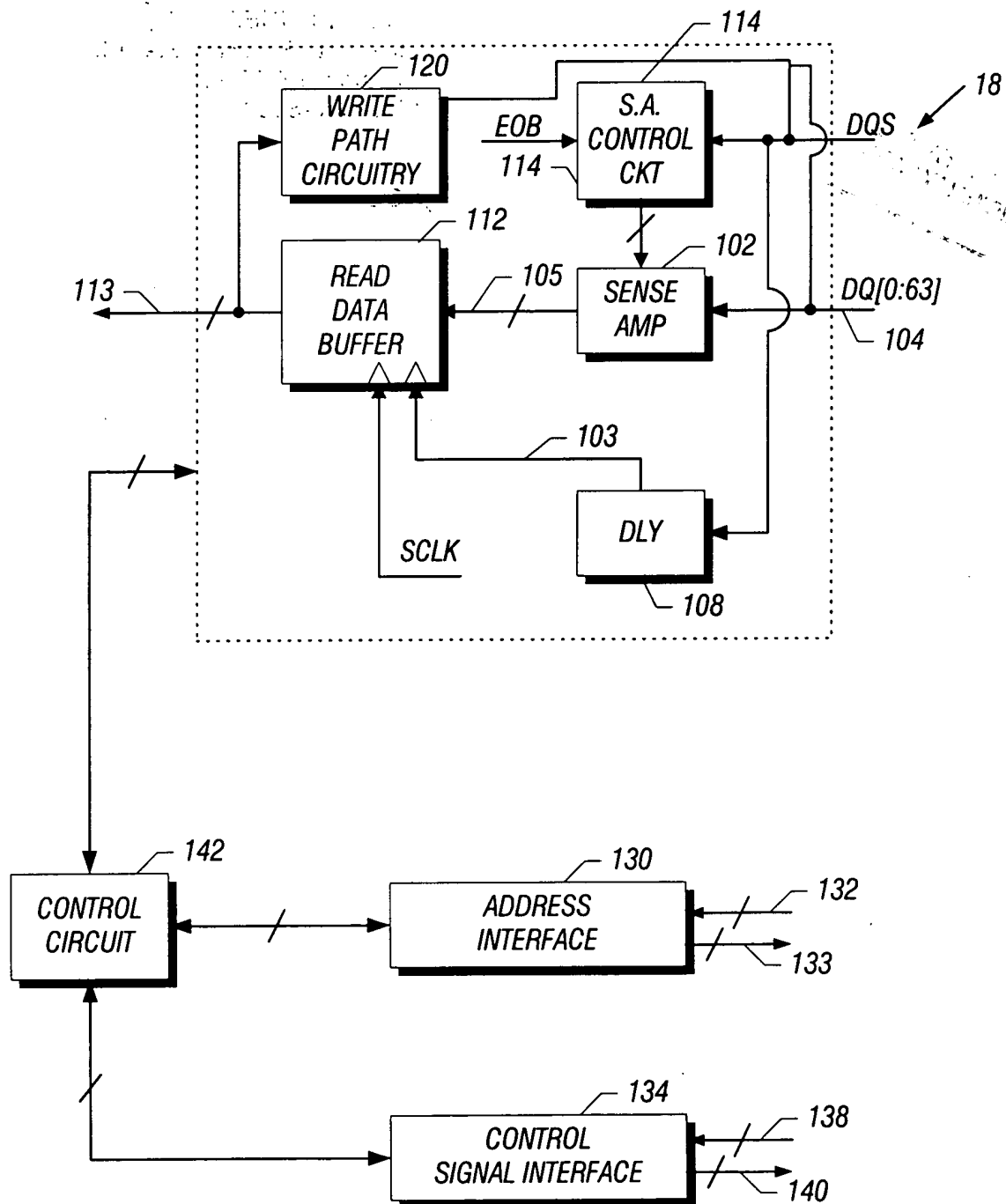


FIG. 3

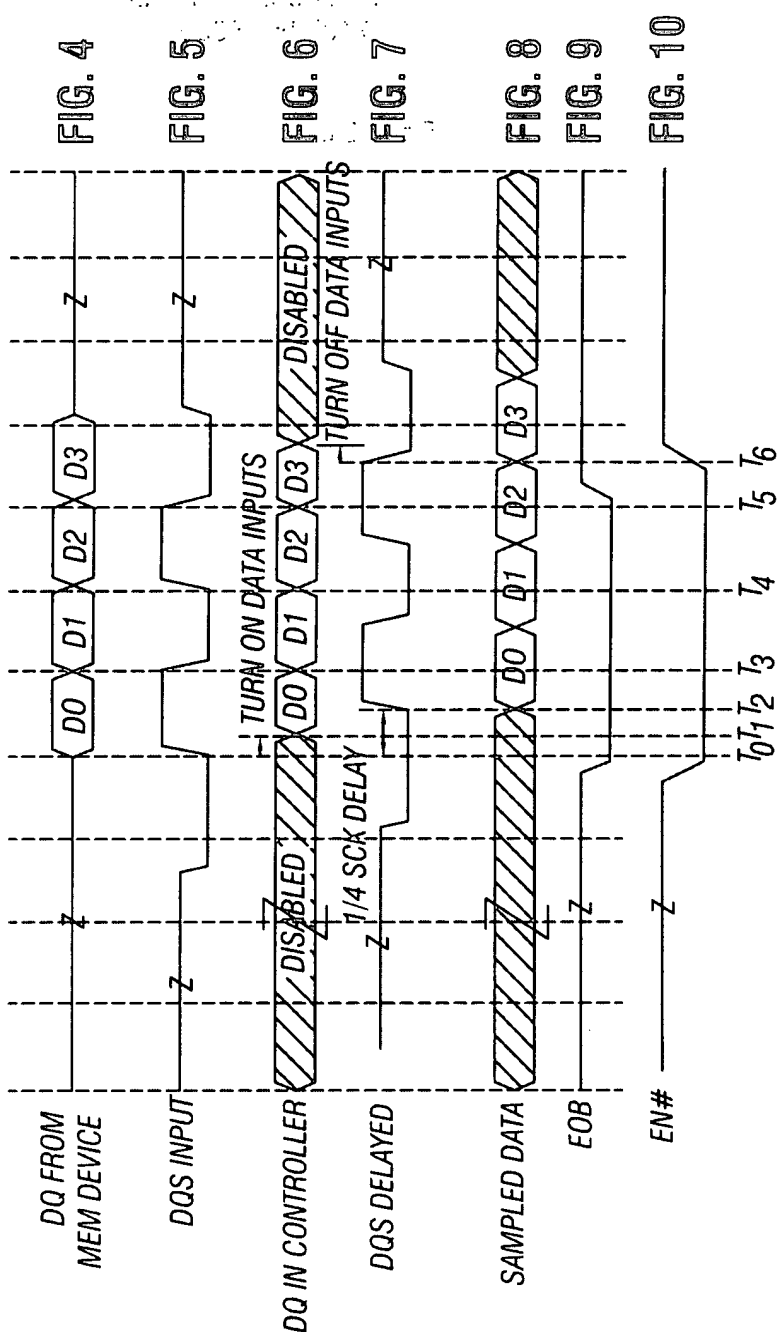


FIG. 4

FIG. 5

FIG. 6

FIG. 7

FIG. 8

FIG. 9

FIG. 10

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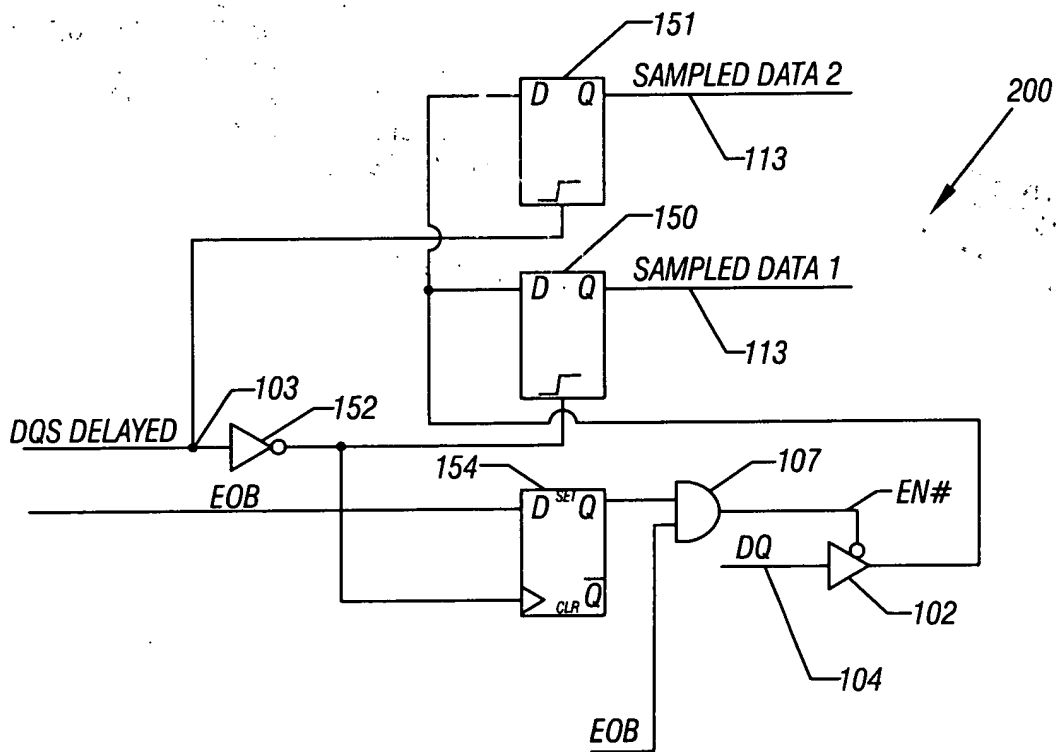


FIG. 11

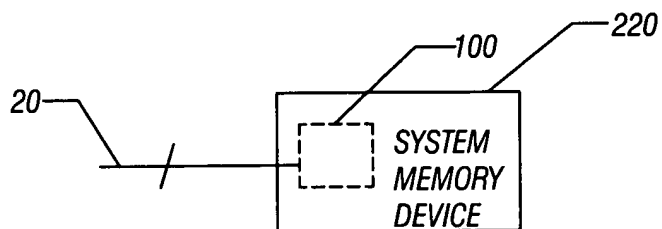


FIG. 12